

APPLICATION DATA SHEET

APPLICATION INFORMATION

Filing Date:: July 29, 2003
Application Type:: Regular
Subject Matter:: Utility
Title:: Column Redundancy Scheme for Non-Volatile Flash Memory Using JTAG Input Protocol

Attorney Docket Number:: X-1173 US
Request for Early Pub?:: No
Request for Non-Pub?:: Yes
Total Drawing Sheets:: 9
Small Entity?:: No
Petition included?:: No

APPLICANT INFORMATION

Applicant Authority Type:: Inventor
Primary Citizenship Ctry:: US
Status:: Full Capacity
Given Name:: Asim
Middle Name:: A.
Family Name:: Bajwa
Street:: 4955 Fontanelle Place
City:: San Jose
State or Province:: CA
Postal or Zip Code:: 95111

Applicant Authority Type:: Inventor
Primary Citizenship Ctry:: Taiwan
Status:: Full Capacity
Given Name:: Ping-Chen
Family Name:: Liu
Street:: 39577 Canyon Heights Drive
City:: Fremont

State or Province:: CA
Postal or Zip Code:: 94539

CORRESPONDENCE INFORMATION

Correspondence Customer Number:: 24309

REPRESENTATIVE INFORMATION

Representative Customer Number::	24309	
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ASSIGNEE INFORMATION

Assignee Name:: Xilinx, Inc.
Street:: 2100 Logic Drive
City:: San Jose
State or Province:: California
Postal or Zip Code:: 95124